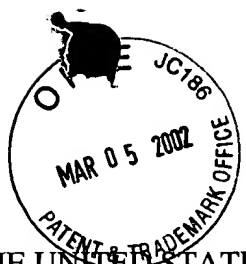


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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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In the Application of:)
)
Hiroyuki Nitta et al.)
)
Serial No.: 09/883,210)
)
Filed: June 19, 2001)
)
For: SEMICONDUCTOR DEVICE)
HAVING A WIRING LAYER OF)
DAMASCENE STRUCTURE AND)
METHOD FOR)
MANUFACTURING THE SAME)

Group Art Unit: 2815
Examiner: J. Nguyen

Atty. Dkt. No. 01701.00086

RESPONSE TO RESTRICTION REQUIREMENT

Assistant Commissioner of Patents
Washington, D.C. 20231

Sir:


This paper is responsive to the Restriction Requirement mailed February 7, 2002 and is filed prior to the expiration of the 30 day shortened statutory period set for response in the Restriction Requirement.

In response to the Restriction Requirement, Applicant elects without traverse the invention of Group I (i.e., claims 1-15) drawn to a semiconductor device, classified in class 257, subclass 750.

Applicant reserves the right to file a divisional application directed to the non-elected invention prior to the termination of proceedings in the present application.

Please charge any fee associated with the filing of this paper to our Deposit
Account No. 19-0733.

Respectfully submitted,

By: 
for Joseph M. Potenza
Registration No. 28,175 *ft 38.800*

BANNER & WITCOFF, LTD.
1001 G Street, N.W., 11th Floor
Washington, D.C. 20001
(202) 508-9100

Dated: March 5, 2002